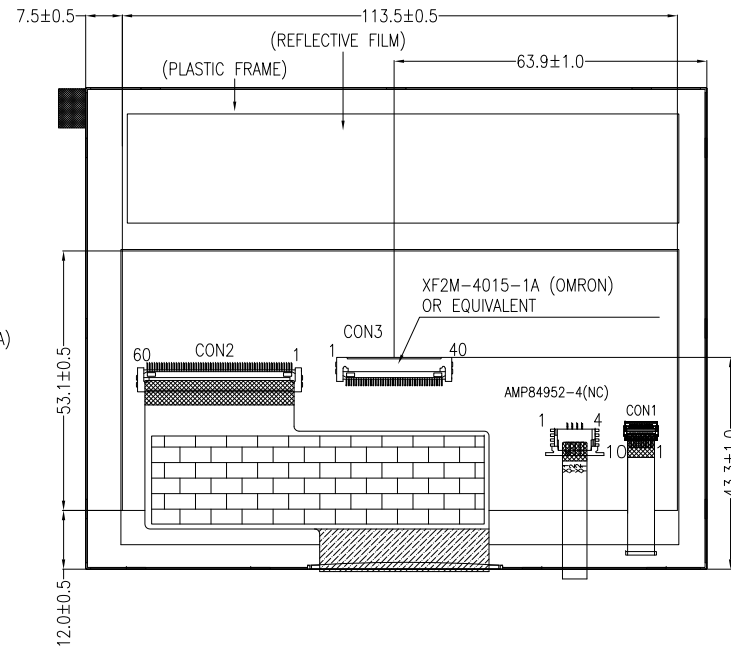
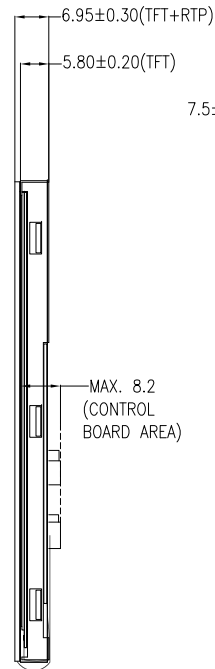
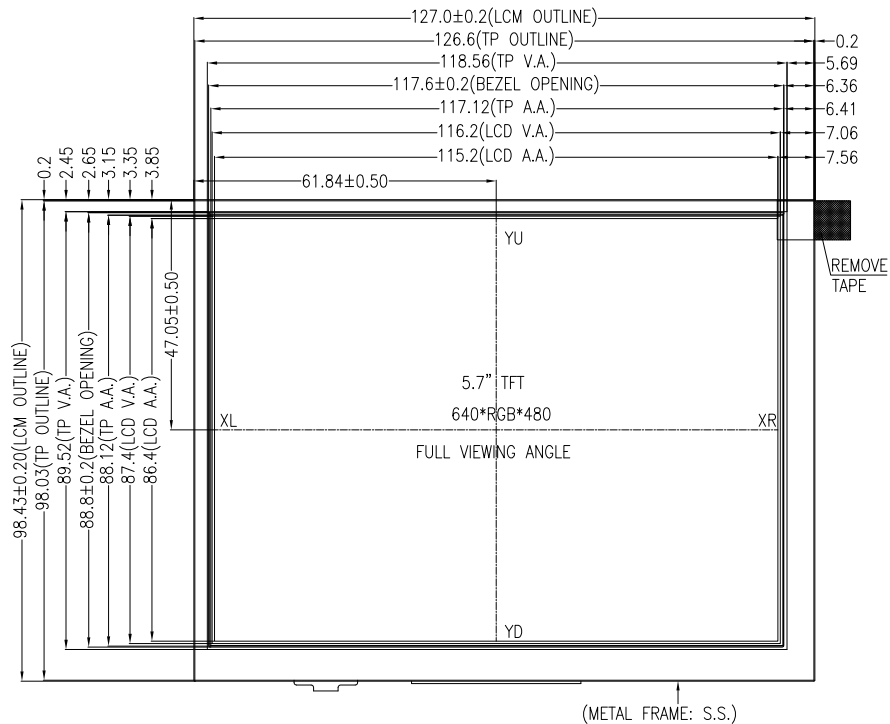


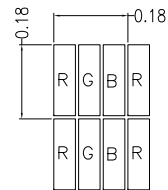
NO MECHANICAL HOUSING TO HOLD THE TFT AND THE TOUCH PANEL ON THIS PRODUCT, SO THE END UNIT'S MECHANICAL DESIGN MUST INCLUDE A HOUSING FOR PREVENTING THE SEPARATION OF THE TFT AND THE TOUCH PANEL.



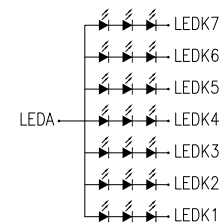
PIN ASSIGNMENT FOR CON3			
PIN	SYMBOL	PIN	SYMBOL
1	U/D	21	G5
2	NC	22	G4
3	HSYNC	23	G3
4	VLED	24	VSS
5	VLED	25	G2
6	VLED	26	G1
7	VCC	27	G0
8	VSYSN	28	VSS
9	DE	29	R5
10	XL	30	R4
11	YU	31	R3
12	ADJ	32	VSS
13	B5	33	R2
14	B4	34	R1
15	B3	35	R0
16	VSS	36	XR
17	B2	37	YD
18	B1	38	DCLK
19	B0	39	VSS
20	VSS	40	L/R

NOTES:

- DISPLAY TYPE: TFT, TRANSMISSIVE, NORMALLY BLACK
- OPERATING VOLTAGE: VCC=3.3V (TYP.)
- VIEWING DIRECTION: FULL VIEWING ANGLE
- DRIVER IC: JD9168S
- OPERATING TEMP.: -20°C ~ 70°C
- STORAGE TEMP.: -30°C ~ 80°C
- LED BACKLIGHT: WHITE LED
BUILT-IN BACKLIGHT DRIVER: VLED=5.0V (TYP.)
- SURFACE LUMINANCE: 540CD/M² (TYP.)
- TOP POLARIZER SURFACE TREATMENT: GLARE
- ONLY THE SINGLE LAYER AREA ON FPC IS BENDABLE AND THE MINIMUM BEND RADIUS IS 1.5MM
- GENERAL TOLERANCE: ±0.3MM
- ROHS COMPLIANT



PIXEL DETAIL



BACKLIGHT CIRCUIT DIAGRAM



MULTI-INNO TECHNOLOGY CO., LTD.

DRAWN BY:	RITA	2024.04.26		MODULE P/N:
CHECKED BY:			SCALE : 1/1	MI0570AET-2RP
APPROVED BY:			UNIT: mm	DESCRIPTION:
VERSION NO:	01		SHEET NO.: 1/1	TFT + RTP MODULE

01	FIRST ISSUE	RITA	2024.04.26
VER.	REVISED DESCRIPTION	REVISER	DATE